	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L2	451922	controller	USPAT; US-PGPUB	2004/09/07 18:48	
2	BRS	L3	17920	control adj processor	USPAT; US-PGPUB	2004/09/07 18:49	
3	BRS	L4	459215	2 or 3	USPAT; US-PGPUB	2004/09/07 18:49	
4	BRS	L5	25710	system adj clock	USPAT; US-PGPUB	2004/09/07 18:50	
5	BRS	L6	24278	multipl$4 near5 clock	USPAT; US-PGPUB	2004/09/07 18:50	
6	BRS	L7	1924	5 same 6	USPAT; US-PGPUB	2004/09/07 18:53	
7	BRS	L8	496124	(input adj3 output) or (I adj2 o)	USPAT; US-PGPUB	2004/09/07 18:54	
8	BRS	L9	152690	synchronous$2	USPAT; US-PGPUB	2004/09/07 18:55	
9	BRS	L10	12300	8 same 9	USPAT; US-PGPUB	2004/09/07 18:55	
10	BRS	L11	146	4 and 7 and 10	USPAT; US-PGPUB	2004/09/07 18:56	
11	BRS	L12	1201	713/400.ccls. 713/401.ccls.	USPAT; US-PGPUB	2004/09/07 18:57	
12	BRS	L13	1216	713/500.ccls. 713/501.ccls.	USPAT; US-PGPUB	2004/09/07 18:57	
13	BRS	L14	977	713/503.ccls. 713/600.ccls.	USPAT; US-PGPUB	2004/09/07 18:57	

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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	L15	2750	12 or 13 or 14	USPAT; US-PGPUB	2004/09/07 18:58	
15	BRS	L16	19	11 and 15	USPAT; US-PGPUB	2004/09/07 18:58	